**[Programmable Communication Group](https://sites.google.com/a/temple.edu/programmable-communication-group/)**

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| Date | Friday, October 4, 2013 | | |
| Advisor | Dr. Silage | | |
| Members | Cedric Destin | Brandon Keith | Brian Thibodeau |

Headline: Website posting due today (10/07/13) and Monday, 10/21/13. Draft Design document due 10/18/13. Second WebEx project review due 10/18/13.

Topics to discuss (with Silage)

* Who should be able to view the WebEx project reviews?
* Preliminary Simulink simulation of 2nd-order Costas loop.

**TBD**

Topics to discuss (among SD team and Silage, if necessary)

* Who on the team will prepare next WebEx design review for 10/21/13?
* Should Cedric implement FSK in simulation of KD2BD transmitter?

Dr. Silage feedback

* The modem specification sheet that was uploaded to GitHub (“demo\_modem\_spec\_sheet.pdf”) is for an RF modem, not a baseband modem.
* Be mindful of Keplerian elements for Doppler shift.

**TBD**

Topics to discuss in next SD meeting

* Implementing PLL in FPGA (besides from Costas loop).

**TBD**

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| **Engineer** | **Status** |
| Brian Thibodeau |  |
| Cedric Destin |  |
| Brandon Keith | * 1st WebEx project review video completed * Working on draft design documentation * Defining specifications for final deliverable(s) * Would like a sample-based Simulink implementation |